

# SILICON RELEASE SOLUTIONS

Access to the broadest, most experienced engineering network, to take your design to a qualified product as fast as possible.

## GLOBAL PRESENCE

- 400+ engineers
- 40 locations (US, Europe, Asia)
- Local support in Asia



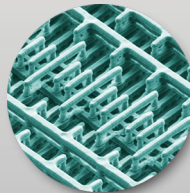
supply chain management

## EXPERIENCE

- 20+ IC markets
- Multiple process technologies
- 50+ years of industry experience



test



failure analysis

## EQUIPMENT

- 500+ unique equipment setups (IC testers, ESD, Environmental Microscopy)



reliability

## PRODUCTION GUARANTEE

- 100+ subcons
- 6000+ testers
- Seamless transfer



production

When you need to find a single partner to help get your new IC product into the market, choose **Silicon Release Solutions** for access to the resources, knowledge, expertise, and equipment needed to make your strategy work.

Silicon Release Solutions is a business group within Teradyne's world-class Global Services Organization. The mission of Silicon Release Solutions is provide a complete and comprehensive set of semiconductor engineering services for companies whose model is to outsource.

Ensuring fabless semiconductor companies get their new products to market and ramping on time.

For more information contact us:

e-mail:  
[srsrfq@teradyne.com](mailto:srsrfq@teradyne.com)

website:  
[www.siliconreleasesolutions.com](http://www.siliconreleasesolutions.com)

For inquiries, please call:  
**+1-408-960-7703**



# COMPLETE VERTICAL SERVICES

For just a few design starts per year, consider outsourcing



## IC Service Segments

DESIGN	PROTOTYPE	QUALIFICATION	PRODUCTION RAMP
<ul style="list-style-type: none"> <li>• IP Selection</li> <li>• Design Partner</li> <li>• Tech. Selection</li> <li>• Pkg. Selection</li> <li>• DFM/DFT</li> <li>• Tester Platform</li> </ul>	<ul style="list-style-type: none"> <li>• Supplier Select</li> <li>• Proto. Mgmt</li> <li>• Pkg Assembly</li> <li>• Functional</li> <li>• Verification</li> <li>• ATE Test Dev.</li> </ul>	<ul style="list-style-type: none"> <li>• Qual. Plan Dev.</li> <li>• Corner Lots</li> <li>• ESD/Latch Up</li> <li>• Temp-Volt. Char.</li> <li>• B/I Life Test</li> <li>• Pkg. Reliability</li> <li>• FA</li> </ul>	<ul style="list-style-type: none"> <li>• Supply Chain</li> <li>• Logistics</li> <li>• Scalability</li> <li>• Inventory/Ship</li> <li>• Yield Improve</li> <li>• QA/Doc Control</li> </ul>

## Earlier Engagement

## IC Release Engagement

SYSTEM ARCHI.	RTL	FRONT END DESIGN	PLACE & ROUTE	GDS II	WAFER	PACKAGE	TEST	ESD L-U	LIFE TEST	CORNER CHARACTERIZATION	SUPPLY DEMAND MGMT	COST REDUCTION	CUSTOMER RELATION
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- **SRS DFT Assessment**
  - Testability analysis of IP and other custom cores
  - Digital and Analog DFT insertion recommendations
  - Tester hardware /configuration recommendations
  - Test list generation
- **SRS DFT Insertion/Scan Diag**
  - Analog/Digital DFT Implementation
- **ESD / LU Review**
- **Reliability Review**

- **Wafer starts**
- **IC Packaging/Assembly**
- **Test HW and SW development and debug**
- **IC characterization (ambient, hot, cold)**
- **ESD, Qualification and Reliability**
- **Pilot production, WS and FT**
- **Transfer to high-volume production**
- **Use scan diagnostic tools to isolate weaknesses in design or process.**



**With an SRS project, you can fill the various functions you need throughout the year, and only when you need them: DFT Engineer, Test Engineer, Product Engineer, Reliability Engineer, Quality/Doc Control, Program Management, Planner/Buyer/Fulfillment**

<http://www.siliconreleasesolutions.com>



e-mail: [srsrfq@teradyne.com](mailto:srsrfq@teradyne.com)  
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