

UPCOMING WEB SEMINARS

Understanding DFT (Design for Test) Methodologies

When: Wednesday, March 22, 2006 at 9:00 AM and 3:00 PM (EST).

Properly designing a PCB for testability means giving early and thorough consideration to both its physical and electrical elements. End-product reliability and manufacturing yields can be adversely effected if the correct DFx rules are not applied to a design. Research proves that late-stage design ECOs caused by the lack of DFx will adversely affect the cost and time required to debug and test a PCB—by orders of magnitude in extreme cases. Join us and discover which rules and layout guidelines will pay the greatest returns. Learn how DFT can give your company a competitive advantage and ensure that your next lead-free, mixed-signal prototype maintains its manufacturing readiness.

Register today at <https://teradyne-atd-events.webex.com>

Topic: The Economics of Automated X-Ray Inspection

When: Wednesday, March 8, 2006 at 9:00 AM and 3:00 PM (EST).

New Automated X-ray Inspection (AXI) systems will change the economics of printed circuit board assembly inspection. This web seminar will compare the costs associated with off-line and in-line (beat rate capable) AXI. Discover just how the cost of inspection is impacted when false failures are reduced with no loss of inspection coverage. Join us and learn how our XStation MX capabilities provide greater return on investment, improve product quality and increase manufacturing productivity.

Register today at <https://teradyne-atd-events.webex.com>

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