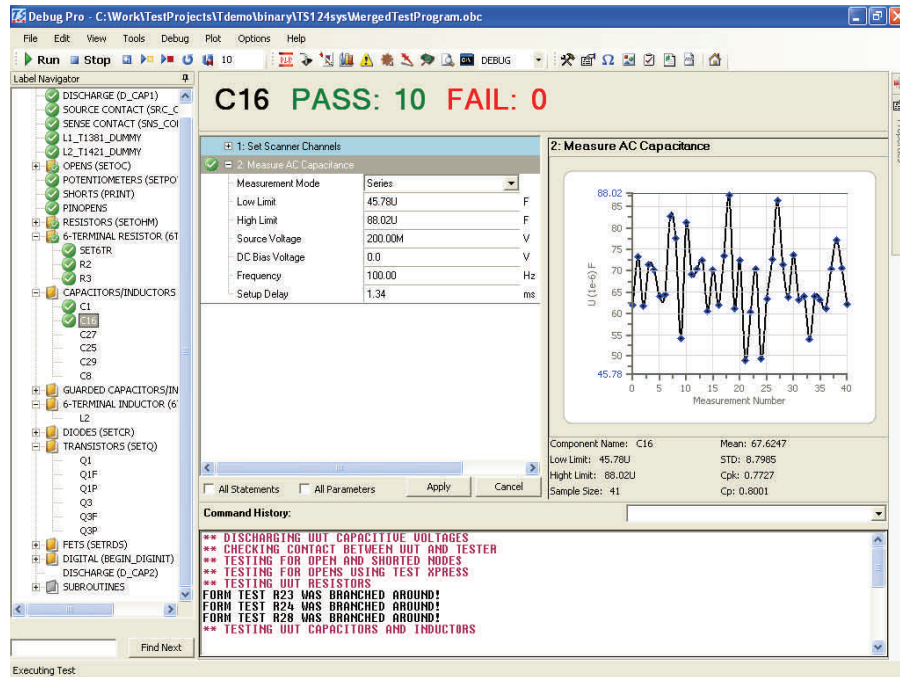


# TestStation Debug Pro

Simple and Powerful Debug Environment for TestStation In-circuit testers

## Key Features:

- Navigate through tests using only a mouse
- Visual icons conveniently display test status
- Views can be customized to display only test parameters of interest
- No need for Operator to learn a test language or debug commands
- Circuit, component, and property information available at operator's fingertips
- Charting tools report test reliability and show test statistics



TestStation Debug Pro helps simply ICT debug and improve program

**T**estStation Debug Pro is Teradyne's new user-friendly interface for debugging, developing and qualifying in-circuit test programs. Its graphical interface incorporates the latest software technologies to provide simple and intuitive access to TestStation's powerful features and debug environment. Key elements of *TestStation Debug Pro* include:

- A **Label Navigator** that graphically displays all the tests in the program. Common component tests for resistors, capacitors, inductors, and others are conveniently grouped in folders. Status icons are used to clearly indicate the Pass/Fail status of each test. Operators can select individual tests to debug by clicking on them or by using built-in search capability.
- A **Statement Editor** displays all of the parameters available for selected tests and allows operators to easily select and modify those parameters using a mouse or simple keystrokes. The display can be further simplified to display only test parameters that the operator typically changes during debug activities.
- A **Guard Editor** window shows test instrument connections and circuit interconnections associated with the device under

test. Operators can easily add and remove test guard points using the mouse.

- A **Charting Tool** displays measurement results using run or histogram charts and reports test statistics like Mean, Std Deviation, Cp, and Cpk. Operators can view the charts to quickly determine the reliability of the test measurements.
- **Component and Property Information** windows display helpful information about board components, values, tolerances, pins, nets, and nails.
- Customizable **Tool Bar** that provides access to powerful debug tools like board and schematic viewers, data collection charts, vectorless test programming, and test quality tools.
- An optional **Untranslator Window** allows long-time experienced users to continue performing debug activities using the familiar commands and keystrokes of the traditional Runtime debug environment.

User benchmarks show that the intuitive operation and advanced capabilities of *TestStation Debug Pro's* user interface help operators complete their debug activities faster with higher quality results.

## Benefits of TestStation Debug Pro:

- Faster learning curve for new users
- Faster access to advanced debug features
- Faster analysis of test measurement results
- Finger tip access to component, property, and connection information
- Automatic circuit analysis and suggested guard points
- Accommodates both experienced and inexperienced test developers

Contact your Teradyne Sales Representative for more information on Teradyne's TestStation Debug Pro.

The image shows two overlapping software windows from Teradyne's TestStation. The background window is titled "Guard Editor - (R23)" and contains configuration options for a test. It has fields for "VOLTAGE SOURCE (A) P5V", "CURRENT MEASURE (B) UN4CONN2652PP", "GUARD SENSE (C) VOUT", and "GUARD (D)". Below these are "Swap" and "Remove All Guards" buttons. A "Selections:" dropdown is set to "Source and Measure". A table lists components:

Device	Type	Pin Count	Value
SOURCE			
C13	C	2	47.00P
R34	R	2	
MEASURE			
R34	R	2	
R36	R	2	

The foreground window is titled "Resistor Test" and displays a circuit diagram. It shows a "DC Voltage Source"  $V_s$  connected to a "Device Under Test" (a resistor) between terminals "SOURCE ChA" and "MEASURE ChB". A "DC Current Measure"  $I_m$  is connected in series with the device. The circuit also includes "Guard Sense ChC" and "Guard Gnd" terminals, with impedances  $Z_{sg}$  and  $Z_{mg}$  shown between the source and sense points. A "Reference" ground is also indicated. The window has "Resistor Test" and "Close" buttons.



Teradyne, Inc.  
Assembly Test Division  
700 Riverpark Drive  
North Reading, MA 01864 U.S.A.  
+1.978.370.2700

TestStation and Framescan are trademarks of Teradyne, Inc. All other brand and product names are trademarks or registered trademarks of their respective owners. Information contained in this document is summary in nature and subject to change without notice. Appearance of the final, delivered product may vary from the photographs shown herein.

© Teradyne 2010—All rights reserved,

[www.teradyne.com/atd](http://www.teradyne.com/atd)

STG-TSP-2010-00